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F-8036

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jae Song CHUNG  
Serial No. : 10/731,943  
Filed : December 10, 2003  
For : SEMICONDUCTOR PACKAGE HAVING GROOVES  
FORMED AT SIDE FLASH, GROOVE FORMING  
METHOD, AND DEFLASHING METHOD USING  
SEMICONDUCTOR PACKAGE FORMED WITH  
GROOVES  
Group Art Unit : 2813  
Examiner : Thanh T. Nguyen  
Confirmation No. : 6748  
Customer No. : 000028107

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T. David Bomzer  
(Name)

  
(Signature)

Commissioner for Patents  
P.O. Box 1450, Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action of June 1, 2005 requiring an election of  
inventions, applicant submits the Remarks presented below.

P8036 rstrc.rpt (PC14).wpd